



1733

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Application No.: 10/028,880

) Date: August 4, 2003

Applicant: Ta-Ko Chuang, et al.

) Examiner: Todd J. Kilkenny

Filed: 12/20/2001

) Group Art Unit 1733

For: "Method for Bonding An Integrated
Circuit Device..."

) Our Ref: 619416-6 B-4442

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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TC 1700

Sir:

This paper is filed in response to the Office Action dated May 2, 2003.
Please amend the claims as indicated below. **All amendments and
remarks herein are made without prejudice.**